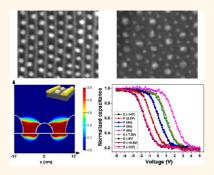
Multicomponent Nanopatterns by Directed Block Copolymer Self-Assembly

Dong Ok Shin,^{†,§,#} Jeong Ho Mun,^{†,‡,#} Geon-Tae Hwang,[†] Jong Moon Yoon,^{†,‡} Ju Young Kim,^{†,‡} Je Moon Yun,^{†,‡} Yong-Biao Yang,[⊥] Youngtak Oh,^{†,‡} Jeong Yong Lee,^{†,‡} Jonghwa Shin,[†] Keon Jae Lee,[†] Soojin Park,[∥] Jaeup U. Kim,[⊥] and Sang Ouk Kim^{†,‡,*}

[†]Department of Materials Science and Engineering, Korea Advanced Institute of Science and Tehcnology (KAIST), Daejeon 305-701, Republic of Korea, [‡]Center for Nanomaterials and Chemical Reactions, Institute for Basic Science (IBS), Daejeon 305-701, Republic of Korea, [§]Power Control Device Research Section, Electronics and Telecommunications Research Institute (ETRI), Daejeon 305-700, Republic of Korea, [⊥]School of Mechanical and Advanced Materials Engineering, Ulsan National Institute of Science and Technology (UNIST), Ulsan 689-798, Republic of Korea, and ^{II}Interdisciplinary School of Green Energy, Ulsan National Institute of Science and Technology (UNIST), Ulsan 689-798, Republic of Korea. [#]These authors contributed equally to this work.

ABSTRACT Complex nanopatterns integrating diverse nanocomponents are crucial requirements for advanced photonics and electronics. Currently, such multicomponent nanopatterns are principally created by colloidal nanoparticle assembly, where large-area processing of highly ordered nanostructures raises significant challenge. We present multicomponent nanopatterns enabled by block copolymer (BCP) self-assembly, which offers device oriented sub-10-nm scale nanopatterns with arbitrary large-area scalability. In this approach, BCP nanopatterns direct the nanoscale lateral ordering of the overlaid second level BCP nanopatterns to create the superimposed multicomponent nanopatterns incorporating nanowires and nanodots. This approach introduces diverse chemical composition of metallic elements including Au, Pt, Fe, Pd, and Co into sub-10-nm scale nanopatterns.



As immediate applications of multicomponent nanopatterns, we demonstrate multilevel charge-trap memory device with Pt—Au binary nanodot pattern and synergistic plasmonic properties of Au nanowire-Pt nanodot pattern.

KEYWORDS: multicomponent · nanopattern · block copolymer · self-assembly

ulticomponent nanopatterns, such as binary superlattices, consist of two or more kinds of chemically or morphologically dissimilar nanocomponents.^{1–5} Unlike single component counterparts, multicomponent nanopatterns offer enormous diversities in the pattern shape, chemical composition and corresponding functionality. Moreover, synergistic interplay of chemically and morphologically dissimilar nanocomponents may create novel structures and properties, which might inspire the development of advanced photonic and electronic devices. To date, periodic assembly of multiple nanocomponents have principally relied on complex colloidal assembly, which is usually mediated by electrostatic forces^{2,6} or biomolecular⁷ or other specific interactions.⁴ Unfortunately, large-area processing of device oriented structures raises significant challenge in those approaches.²⁻⁷

Downscaling traditional photolithography below 10 nm scale is suffering from intrinsic optical resolution limit. Numerous alternative or complementary technologies are under development to overcome this major technological challenge. Block copolymer (BCP) self-assembly provides lithography templates for dense sub-10-nm scale features with high-throughput manufacturability, which is hardly attainable by other patterning methods.^{8–20} Furthermore, directed self-assembly integrated with conventional photolithography offers laterally ordered device oriented nanopatterns, which holds great promise for cost-effective nanopatterning.^{13,14,20–28}

In this work, we introduce BCP selfassembly as an alternative method for multicomponent nanopatterns. Generally, the selfassembled nanopatterns in BCP thin films consist of single-shape monodisperse nanodomains of spheres, cylinders, or lamellae. Therefore, it is hard to integrate multiple nanocomponents. We develop a multilevel approach to address this limitation and successfully generated multicomponent nanopatterns. Our approach is differentiated from

* Address correspondence to sangouk.kim@kaist.ac.kr.

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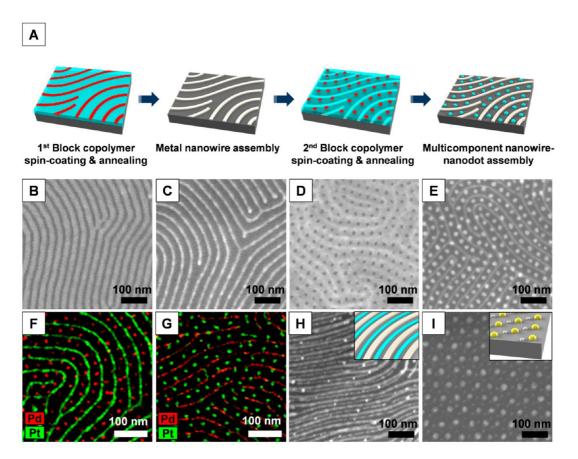


Figure 1. (A) Schematic procedure of multicomponent nanopatterning. (B) The first level BCP self-assembly for surface parallel cylinders. (C) Pt metallization of parallel cylinders. (D) Second level BCP self-assembly for vertical cylinders. (E) Pd metallization of vertical cylinders to complete Pt nanowire–Pd nanodot multicomponent nanopattern. EDS elemental mapping of (F) Pt nanowire–Pd nanodot array and (G) Pd nanowire–Pt nanodot array. Multicomponent nanopatterns of (H) Pt–Pd nanowire array and (I) Au–Pt nanodot array.

the previous approaches relying on complex nanoparticle assembly as follows. The well-established parallel process steps based on BCP self-assembly are genuinely scalable to arbitrary large area processing. In addition, the lateral ordering of multicomponent nanopatterns is precisely controllable by directed selfassembly methods available for BCP self-assembly. Finally, the sizes of nanocomponents are independently tunable without influencing the pattern periodicity.

RESULTS AND DISCUSSION

Figure 1A illustrates our multicomponent nanopatterning procedure. In the first level self-assembly, asymmetric polystyrene-*block*-poly(4-vinylpyridine) (PS-*b*-P4VP) BCP thin films were deposited onto a substrate. The subsequent solvent annealing induced P4VP nanocylinder array, whose orientation was controllable by solvent vapor and annealing time.^{29–32} Tetrahydrofuran (THF) vapor treatment typically generated monolayer nanocylinders oriented parallel to the substrate (Figure 1B).³⁰ We note that a prolonged annealing could induce the transition of BCP morphology from parallel to perpendicular cylinder orientation. However, this transition takes several hours, which affords sufficient time to select desired parallel orientation.³¹ The BCP thin film was then immersed in an acidic aqueous solution of metal precursor, where anionic metal precursors were specifically associated with the protonated pyridinic nitrogen in P4VP nanocylinders.³²⁻³⁴ Oxygen plasma etching over the entire area selectively removed the organic components, while leaving the metal nanowire array at the locations of the P4VP cylinders. Figure 1C shows the resultant Pt nanowire array (34 nm period and 10 nm width), prepared from an aqueous solution of Na₂PtCl₄. After this first self-assembly cycle, the second BCP layer was overlaid on the Pt nanowire pattern by spin casting and annealed under THF/toluene mixture vapor. The mixture vapor induced vertical nanocylinders,²⁹ accurately registered between Pt nanowires (Figure 1D). This vertical nanocylinder morphology is permanently stable regardless of annealing time. The subsequent pattern transfer with Na₂PdCl₄ aqueous solution achieved the multicomponent nanopattern of Pd nanodot arrays alternated with Pt nanowires (Figure 1E). Figure 1F shows the energy-dispersive X-ray spectroscopy (EDS) element mapping results. The Pt component in the nanowires clearly contrasts with the Pd component in the nanodots (Supporting Information, Figure S1). A simple exchange of the metal precursor loading sequence resulted in the multicomponent nanopattern

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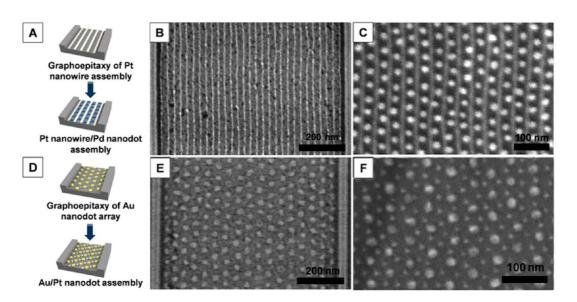


Figure 2. (A and D) Schematic procedure of directed self-assembly by two step process. Laterally ordered nanopatterns of (B) Pt nanowire array, (C) Pt nanowire–Pd nanodot array, (E) Au nanodot array, and (F) Au–Pt nanodot array.

of Pd nanowires and Pt nanodots (Figure 1G, Supporting Information, Figures S1 and S2).

The controllability of nanocylinder orientation with solvent vapor could diversify the attainable nanopatterns. Figure 1H shows a Pt-Pd nanowire assembly achieved from the two self-assembly cycles with THF vapor treatments. The width of nanowires for each metallic component was independently tunable with the concentration and the loading time of the metal precursor solutions (Pt nanowires, 10 nm; Pd nanowires, 8 nm in Figure 1H). Figure 1I presents the Au-Pt binary nanodot array achieved via the two self-assembly cycles with mixture vapor treatments. Owing to the various available ionic metal complexes, multicomponent nanopatterns can incorporate a broad spectrum of metallic elements (Supporting Information, Figures S3 and S4). Additionally, other BCPs can be employed for multilevel process, including widely used polystyreneblock-poly(methyl methacrylate) (PS-b-PMMA), which generally self-assembles via thermal annealing rather than solvent annealing (Supporting Information, Figure S5).

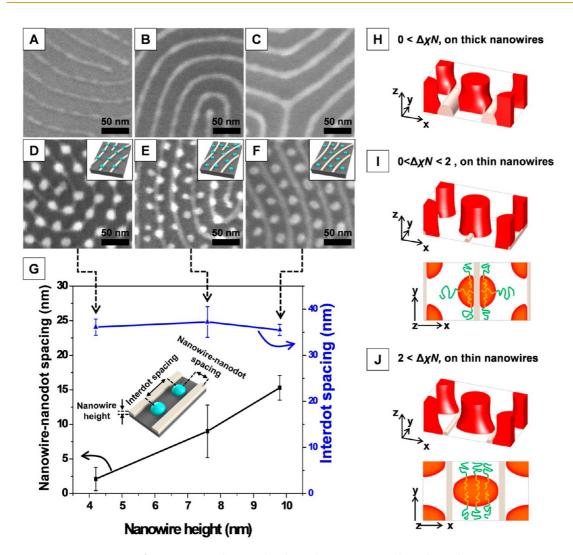
Our multicomponent nanopatterning is compatible with various directed self-assembly methods exploiting chemically or topographically prepatterned surfaces.^{13,14,35–37} It is noteworthy that directed selfassembly has been employed to create laterally ordered parallel line or hexagonal nanodot patterns in block copolymer thin films *via* solvent annealing as well as thermal annealing.^{15,24} In this work, directed self-assembly enabled the controllability of the orientation and lateral ordering of multicomponent nanopatterns. Figure 2 presents multicomponent nanopatterns directed by graphoepitaxy employing topographically prepatterned substrates.^{23,27} The nanodomain ordering of the first self-assembly was directed by the graphoepitaxy effect from the topographically prepatterned substrates (Figures2B,E). Solvent annealing of PS-b-P4VP thin films within topographic trenches and subsequent metallization generated aligned Pt nanowire arrays (Figure 2B) or hexagonal Au nanodot arrays (Figure 2E). The second self-assembly of the overlaid PS-b-P4VP thin films was registered by the bottom metal nanowire or nanodot array generated in the first self-assembly step (Figure 2C,F). Final pattern transfer by metallization generated graphoepitaxially ordered multicomponent nanopatterns of Pd nanodot arrays alternated with Pt nanowires (Figure 2C) or Pt-Au binary nanodot array (Figure 2F). Such self-registration behavior can be further exploited for the laterally ordered multicomponent nanopatterns over a large-area in conjunction with various directed selfassembly approaches. 13, 14, 20-28

The chemical composition and the characteristic dimension of nanocomponents generated in the first self-assembly cycle play significant roles for the subsequent registration of the second overlaid self-assembly (Supporting Information, Figures S6 and S7, Figure 3). When thin Pt nanowires with 4.2-nm height and 6.5 nm width were prepared in the first self-assembly, the Pd nanodots from the subsequent step were spontaneously registered along the nanowires (Figure 3A,D). As nanowire became thicker (Figure 3B,C), Pd nanodots gradually migrated into the space between neighboring nanowires (Figure 3E,F). Figure 3G summarizes the variation of wiredot spacing and the corresponding interdot spacing. In contrast to the morphology evolution with Pt nanowires, when Au nanowires were prepared in the first selfassembly, Pd nanodots were located in the space between nanowires regardless of Au nanowire dimension (Supporting Information, Figure S8).

The interesting morphology variation was analyzed with self-consistent field theory (SCFT) calculation.^{38,39}

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Figure 3. (A–C) SEM images of Pt nanowires with various heights and (D–F) corresponding Pd nanodot registration. (G) Wiredot spacing and interdot spacing plotted against the nanowire height. (H–J) SCFT calculation of the BCP thin film selfassembled on substrates with parallel nanowires. (H) Vertical cylinders assembled in the space between 'thick' nanowires ($0 < \Delta \chi N$). (I) Vertical cylinders assembled on 'thin' nanowires ($\Delta \chi N < 2$). (J) Vertical cylinders located in the space between 'thin' nanowires ($2 < \Delta \chi N$). If $0 < \Delta \chi N < 2$, vertical cylinders migrate from the top of nanowires to the space between nanowires with nanowire thickness.

The PS-b-P4VP BCPs were modeled as AB Gaussian chains with $\chi_{AB}N = 25$ and A (P4VP) fraction of f = 0.3, where χ_{AB} is the Flory–Huggins interaction parameter and N is the number of segments in a polymer chain. When this asymmetric BCPs self-assembled into cylinder monolayer oriented parallel to the substrate, the center-to-center distance between neighboring cylinders was calculated to be $L_0 = 1.64aN^{1/2}$ (34 nm in experiment), where a is the statistical segment length. We calculated the equilibrium morphology of the 0.73L₀ (25 nm)-thick BCP films on the substrate with parallel nanowire array, whose period was consistent to L_0 . The bottom Si substrate was assumed to be weakly preferential for P4VP block. We used two different half-elliptical shapes of the surface nanowires (taken from the nanowire dimensions measured in the experiment), whose lateral widths were 0.12Lo and 0.37L₀, and heights were 0.091L, and 0.27L₀, respectively.

The nanowires were assumed to be filled with immobilized C homopolymers, and the balance of nanowire—polymer interfacial interaction was controlled by interaction contrast, $\Delta \chi N = \chi_{AC} N - \chi_{BC} N$ (see Methods section for details).

SCFT simulation presented that given the bottom surface weakly preferential for P4VP, the registration of P4VP cylinder is dominated by $\Delta \chi N$. If nanowires preferred P4VP ($\Delta \chi N < 0$), P4VP cylinders were registered on the nanowires (Supporting Information, Figure S9), In contrast, if nanowires strongly preferred PS ($\Delta \chi N > 2$), cylinders were registered in the space between nanowires, as observed in the experimental results with Au nanowires (Figure 3H,J, and Supporting Information, Figure S10). An exception was observed when nanowires are thin and weakly preferred PS ($0 < \Delta \chi N < 2$), which corresponds to the case with thin Pt nanowires in the experiments (Figure 3H,I). A close

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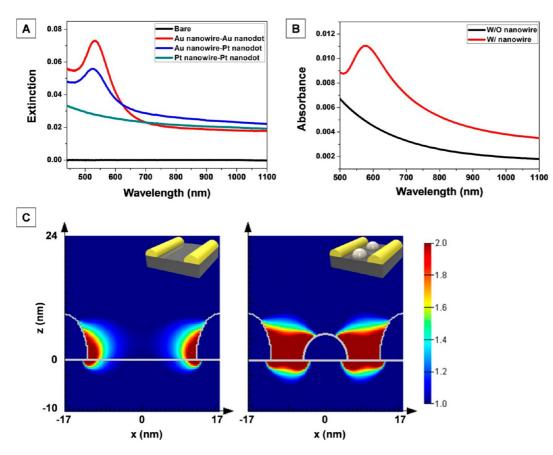


Figure 4. (A) Extinction spectra of various metal nanowire—nanodot arrays. (B) FDTD calculation of light energy absorption at Pt nanodots with or without Au nanowires. (C) The simulated localized electric field intensities normalized with excitation plane wave. Right panel shows the feeding of intensified field into Pt nanodots.

observation of Figure 3I reveals that the bottom part of vertical P4VP cylinders are significantly deformed when cylinders are registered between nanowires. This distortion is caused by the preferential alignment of polymer chains enforced by neighboring bottom nanowires. Since this distortion accompanies the enthalpy penalty arising from the increased cylinder surface area along with the entropy penalty from polymer chain alignment, it is avoided particularly when $\Delta \gamma N$ is insufficiently large and nanowires are thin. The effect from $\Delta \gamma N$, which weakly prefers PS, could be overcome such that P4VP cylinders are registered along the thin nanowires. As the nanowires became thicker, the P4VP cylinders eventually migrated into the space between nanowires due to the additional enthalpy gain from the enlarged nanowire-PS interfacial area (Figure 3H).

The ability to incorporate multicomponents into a nanopattern offers diverse opportunities in terms of applications. For a photonic application, one component may be designed to serve as an "optical antenna" that efficiently converts incoming optical energy into localized and intensified electromagnetic fields, while another component can act as a localized hotspot or "photon sink". Alternating Au nanowire—Pt nanodot array is a good candidate for such a system. In nearinfrared (IR) and visible wavelengths longer than 600 nm,

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Au with a relatively small imaginary part of electric permittivity can be used as an optical antenna, whereas Pt is a good absorber with a large imaginary permittivity.

Figure 4A shows the experimental extinction spectra of multicomponent nanopatterns over visible and IR wavelengths. In contrast to the Au-only or Pt-only array, the combination of Au nanowire-Pt nanodot array exhibited the highest degree of extinction in the broad wavelength range from 620 to 1100 nm. The synergistic interplay between distinct metallic components led to the extinction largely improved from the average of Au-only and Pt-only arrays. A numerical analysis based on a finite-difference time-domain scheme (Figure 4B) provided light absorption at the nanodots selectively, excluding the absorption at the nanowires.^{40,41} The greatly enhanced absorption in the presence of nanowires was caused by the intensified local fields from the nanowires. In Figure 4C, the left panel illustrates the localized electric field intensity in the proximity of nanowires, while the right panel shows the intensified fields feeding of Pt nanodots. Similar synergistic plasmonic effect can be utilized in applications other than simple light absorption, such as sensors, photocatalysis and solar energy utilization, where light functions as an energy or excitation source.42-44

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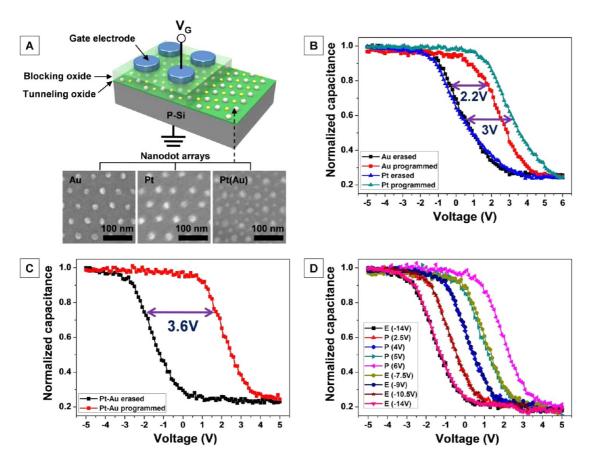


Figure 5. (A) Schematic illustration of charge trap memory device structure. Capacitance–voltage responses of the devices with (B) Pt or Au nanodot array and (C) Pt–Au binary nanodot array. (D) Multilevel data storage capacity of the device with Pt–Au binary nanodot array.

We demonstrate another potential application of multicomponent nanopatterns, where binary nanodot array was employed as charge-trapping layer for nonvolatile flash memory. Presently, widely used film type floating gate frequently suffers from charge leakage, which is a significant bottleneck for the further downscaling of the memory cell dimension. Charge trap layer consisting of discrete metal nanodot array may address such an inherent limitation.^{45,46} To date, there have been several reports that utilize the metal nanostructures for charge trap memory devices.^{47,48} While those works successfully introduced the charge trapping layers based on metal nanostructures, their processes revealed significant limitations for the scalability to large-area and compatibility with industrial process. By contrast, our work provides a well-established route to overcome such limitations. In addition, memory window could be controlled over a wide range with a precise tunability by employing a variety of multicomponent combinations.

Figure 5A schematically illustrates the device architecture, where single or multiple nanodot array was generated on the 3-nm-thick Al₂O₃ tunneling oxide as a charge trapping layer. The capacitance–voltage responses of the memory devices are plotted in Figure 5B,C upon programming/erasing operation at 1 MHz. The programmed and erased states were switched by applying bias pulses of 7 V for 50 ms and -13 V for 30 ms at the top gate electrodes, respectively. The Pt nanodot device showed a memory window (3.0 V) larger than that of the Au nanodot device (2.2 V) due to the different potential well depths determined by the workfunction and nanodot size.⁴⁹

In contrast, the Pt—Au binary nanodot device showed a greatly increased memory window of 3.6 V (Figure 5C). A higher spatial density of Pt—Au binary nanodot optimized the charge storage capacity and allowed such a larger memory window for charge trap memory. Figure 5D shows the multilevel data storage capacity of the Pt—Au binary nanodot device. The large memory window stabilized five distinguished data states with a proper memory window step (~0.8 V). The uniform distribution and relatively large separation of nanodots (>10 nm) hinder interparticular charge leakage and stabilize the memory operation. Precise tunability of charge trap characteristics was achieved with the combination of nanopatterned multimetallic elements.

CONCLUSIONS

We have demonstrated that BCP self-assembly can create self-registered multicomponent nanopatterns consisting of metal nanowires and nanodots. Directed

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self-assembly can create laterally ordered multicomponent nanopatterns, which is hardly attainable by complex colloidal assembly. Significantly, the potential applications of multicomponent nanopattern have been successfully demonstrated for multilevel switching charge trap memory devices and synergistic plasmonic properties. This multicomponent nanopatterning relying on the multilevel self-assembly manifests how to extend periodic molecular self-assembly for diverse

METHODS

Materials. Asymmetric block copolymers (BCPs), polystyreneblock-poly(4-vinylpyridine)s (PS-b-P4VPs, M_{n} : 24 kg/mol for PS, 9.5 kg/mol for P4VP and 41.5 kg/mol for PS, 17.5 kg/mol for P4VP) were purchased from Polymer Source, Inc. Na₂PtCl₄·H₂O, Na₂PdCl₄·3H₂O, HAuCl₄, K₃Fe(CN)₆), and K₃Co(CN)₆ were purchased from Strem Chemicals.

Preparation of Multicomponent Nanopatterns. The silicon wafers were immersed in a piranha solution (7:3 mixture of H₂SO₄ and H₂O₂) for 1 h at 110 °C and then washed several times with deionized water. PS-b-P4VP BCPs (0.5 wt %) were dissolved in toluene/THF solvent mixtures (80/20, v/v). To fabricate the multicomponent assembly, the first level BCP self-assembly began with 30-nm thick PS-b-P4VP thin films spin-coated from a 0.5 wt % toluene/THF solution onto cleaned silicon wafers. The as-spun film was solvent-annealed in a small closed vessel. A homogeneous liquid mixture of THF/hexane (70/30 v/v) or THF/ toluene (80/20 v/v) was then injected into the vessel. The small vessel was saturated with the spontaneously evaporated solvent vapor in several minutes at room temperature (25 ± 2 °C). The as-spun films were then annealed for 1-5 h to develop the highly ordered cylindrical nanodomains. The solvent-annealed samples were then immersed in an HCl (ag) solution of a metal ion complex at a desired concentration for a given loading time. After metal ion loading, the samples were rinsed with deionized water several times to remove excess amounts of metal ions and were dried with N_2 gas. An oxygen plasma treatment removed the polymer template and left the metal nanopatterned structures on the silicon substrate. For the second level of BCP self-assembly, PS-b-P4VP BCP thin film with a thickness of 30 nm was spin-coated from a 0.5 wt % toluene/THF solution onto metal nanopatterned surfaces. Solvent annealing was then carried out under THF/hexane vapor for in-plane cylinder arrays, or THF/toluene vapor was used for out-of-plane cylinder arrays. After an oxygen plasma treatment following the second metal loading step, a variety of multicomponent assemblies including nanowire-nanodots, nanowire-nanowires and nanodotnanodot assemblies were obtained.

Fabrication of Silicon Trench. A negative-tone photoresist of SU8 (MicroChem Corp. US) was used to prepare the topographic photoresist confinements. A 180 nm-thick photoresist layer was spin-coated onto a cleaned silicon substrate and soft baked at 65 °C for 20 s. The photoresist film was exposed to an I-line source (Midas/MDA-6000 DUV, KR; wavelength: 365 nm; 9.5 mW/cm²) through a pattern mask and post baked at 110 °C for 60 s to cross-link the exposed portion of the film selectively. The pattern development process was performed by immersing the exposed photoresist film into a propylene glycol methyl ether acetate (PGMEA) solution for 60 s. Trench patterns with a depth 40 nm were fabrication by SF₆ reactive ion etching.

Fabrication of Charge Trap Memory. The p-type Si (100) substrates were cleaned by a piranha solution (4:1 mixture of H_2SO_4 and H_2O_2) at 150 °C and then dipped into a buffered oxide etchant (BOE) to remove any residual native oxide. An AI_2O_3 tunneling oxide (3 nm) layer was deposited on the Si substrates by atomic layer deposition (ALD) and then a multimetal assembly was prepared using multistep, self-registering BCP selfassembly. Next, a 19-nm AI_2O_3 blocking oxide layer was deposited via ALD under the same conditions used for the tunneling pattern shapes, characteristic dimensions, and chemical compositions effectively. The realized synergistic properties, which surpass the simple additive characteristics of constituting nanocomponents, hold great promise for various applications demanding complex architectures with multifunctionalities. Further advance of this approach to integrate semiconductors and other functionalities would greatly strengthen the potential of self-assembled nanopatterning.

oxide layer and 5 nm/100 nm Ti/Pt layers were deposited by reactive radiofrequency magnetron sputtering. The lift-off process was performed to define the round Ti/Pt gate electrodes.

Characterization. The nanoscale morphology of the BCP thin films and metal nanopatterned surfaces was characterized using a Hitachi S-4800 FE-SEM. STEM characterization was performed using a JEOL JEM-ARM200F equipped with an EDS detector. XPS measurements were performed using ESCA 2000 system (Thermo VG Scientific). The height profiles of the metal nanopatterns were analyzed by AFM (Park System, Korea). The UV–vis extinction spectrum of the metal nanopatterns was measured using a UV–vis spectrophotometer (Jasco, V-530). The capacitance–voltage curves of the charge trap memory were measured at 1 MHz by a HP 4194A impedance/gain-phase analyzer at room temperature. The conduction properties were measured using a Keithley 4200-SCS semiconductor characterization system at room temperature.

Self-Consistent Field Theory (SCFT) Calculation. The statistics of the AB BCPs were calculated by the standard SCFT method with modifications given below. To exclude the BCPs from the interior of bottom nanowires, C homopolymers with the same length *N* were introduced, which were confined within the nanowires due to large external fields ($w^{\text{ext}} = 50$ inside the nanowires).⁵⁰ This method guaranteed that the BCP domains have a smooth density transition, and it also allowed us to contrast the A block–nanowire interaction and B block–nanowire interaction by modifying $\chi_{BC}N$ value from 21 to 27, while maintaining $\chi_{AC}N = 25$. The weak preference of bottom substrate (silicon substrate in our experiments) to P4VP domain was assigned by setting $\eta = 0.1$ in the following self-consistency equation:

$$w_{A}(\mathbf{r}) = -2\eta\delta(z)aN^{1/2} + \chi_{AB}N\phi_{B}(\mathbf{r}) + \chi_{AC}N\phi_{C}(\mathbf{r}) + \xi(\mathbf{r})$$

$$w_{B}(\mathbf{r}) = \chi_{AB}N\phi_{A}(\mathbf{r}) + \chi_{BC}N\phi_{C}(\mathbf{r}) + \xi(\mathbf{r})$$

$$w_{C}(\mathbf{r}) = \chi_{AC}N\phi_{A}(\mathbf{r}) + \chi_{AC}N\phi_{D}(\mathbf{r}) + \xi(\mathbf{r}) + w^{\text{ext}}(\mathbf{r})$$

where $\phi_i(\mathbf{r})$ is the segment density of *i* type polymers, and $w_i(\mathbf{r})$ is the field acting on it. The simulation box consisted of 3-dimensional $128 \times 128 \times 128$ grid, and the modified diffusion equation for the partition functions of BCPs and homopolymers were solved using the pseudospectral method. $^{\rm 51,52}$ For both BCPs and homopolymers, 100 mesh points were used in the polymer length direction. The Neumann boundary condition was used for all boundaries to correctly simulate the hexagonal geometry while minimizing the unit cell size. For the first level nanopatterning, we assumed thinnest cylindrical monolayers were selfassembled, which suggested that the nanowire period was $1.64aN^{1/2}$. Because of the substrate pattern, the morphology of the second level nanopatterning deviated from that of the hexagonal cylinder phase; hence, we varied the system size in y direction until minimum free energy was achieved. To find the equilibrium morphology, we basically tested two competing morphologies, one with vertical cylinders on top of the nanowires and the other with vertical cylinders in between the nanowires. As expected, P4VP cylinders preferring nanowires ($\Delta \chi N < 0$) stood on top of the nanowires regardless of the nanowire size (Figure S7). It is also natural that when P4VP cylinders have strong repulsion with nanowires, $\Delta \chi N > 2$, they migrate into the gap between nanowires (Figure S8). The nonintuitive experimental results for Pt were reproducible in the range, $0 < \Delta \chi N < 2$. In this case, the contact

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Finite-Difference Time-Domain (FDTD) Calculation. A finite difference time-domain method was adopted for the numerical investigation of the optical properties of the metallic nanostructures.⁵³ It utilized Yee's discretization scheme, and its accuracy and convergence conditions are well understood.⁵⁴ The optical properties of the Au and Pt were taken from tabulated values⁵⁵ and approximated with analytic functions. The domain of the simulation had the same lateral sizes as the periods of the fabricated sample, and the periodic boundary condition was utilized.⁵⁴ The vertical size was 200 nm, as terminated by perfectly matched layers. The computational domain was discretized in 0.25 nm steps inside the metall regions and in progressively larger steps away from the metallic structures, with a maximum step size of 34 nm. Mirror symmetries were exploited to reduce the computation domain by 2- or 4-fold when possible.

Conflict of Interest: The authors declare no competing financial interest.

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Supporting Information Available: STEM images of multicomponent nanopattern with elemental mapping; quantitative analysis of metallic nature of Pt nanowire—Pd nanodot multicomponent nanopattern; Broad field SEM images of multicomponent nanopattern; multicomponent nanopattern achieved with PS-b-PMMA thin film, self-assembled via thermal annealing; SCFT calculation of the self-assembled PS-b-P4VP thin film morphologies on the substrate with thin and thick metal nanowires having preferred wetting for P4VP or for PS matrix. This material is available free of charge via the Internet at http://pubs.acs.org.

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